



#### GPIO use guide:

1. Note that the voltage of SOC GPIO must matches the external IO voltage.
2. The pull up voltage of the GPIO is selected to correspond to the power field voltage of GPIO.

U1B	
P0/NAND_WE/SDC2_D/SPI0_CLK/PC_EINT0	PH0/UART0_TX/CAN_TX/PWM3/TW11_SCK/PH_EINT0
P1/NAND_ALE/SDC2_RST/PC_EINT1	PH1/UART0_RX/CAN_RX/PWM4/TW11_SDA/PH_EINT1
P2/NAND_CE/SPI0_MOSI/PC_EINT2	PH2/UART5_TX/SPDIF_CLK/PWM2/TW12_SCK/PH_EINT2
P3/NAND_CS0/BOOT_SEL1/PC_EINT3	PH3/UART5_RX/SPDIF_IN/PWM4/TW12_SDA/PH_EINT3
P4/NAND_CE0/SPI0_MISO/BOOT_SEL2/PC_EINT4	PH4/SPDIF_OUT/TW13_SCK/PH_EINT4
P5/NAND_CS1/BOOT_SEL3/PC_EINT5	PH5/UART2_TXH_I2S3_MCLK/SPI1_CS0/TW13_SDA/PH_EINT5
P6/NAND_RB0/SDC2_CMD/BOOT_SEL4/PC_EINT6	PH6/UART2_RXH_I2S3_BCLK/SPI1_CLK/TW14_SCK/PH_EINT6
P7/NAND_RB1/SPI0_CS1/PC_EINT7	PH7/UART2_RTSH_I2S3_LRCK/SPI1_MOSI/TW14_SDA/PH_EINT7
P8/NAND_DQ0/SPI0_CS2_D/SPI0_HOLD/PC_EINT8	PH8/UART2_CTSH_I2S3_DOUT/SPI1_MISO/H_I2S3_DIN1/PH_EINT8
P9/NAND_DQ1/SPI0_CS2_D/SPI0_HOLD/PC_EINT9	PH9/H_I2S3_DINO/SPI1_CS1/H_I2S3_DOUT1/PH_EINT9
P10/NR_RX/TOON_TRIGGER/PC_EINT10	PH10/R_RX/TOON_TRIGGER/PC_EINT10

P10/RGMII_RXD3/RMII_NULL/DMIC_CLKH_I2S0_MCLK/HDMI_SCLK/PI_EINT0	P10/RGMII_RXD3/RMII_NULL/DMIC_CLKH_I2S0_MCLK/HDMI_SCLK/PI_EINT0
P11/RGMII_RXD2/RMII_NULL/DMIC_DATA1H_I2S0_BCLK/HDMI_SDA/PI_EINT1	P11/RGMII_RXD2/RMII_NULL/DMIC_DATA1H_I2S0_BCLK/HDMI_SDA/PI_EINT1
P12/RGMII_RXD1/RMII_RXD1/DMIC_DATA1H_I2S0_LRCK/HDMI_CECPI_EINT2	P12/RGMII_RXD1/RMII_RXD1/DMIC_DATA1H_I2S0_LRCK/HDMI_CECPI_EINT2
P13/RGMII_RXD0/RMII_RXD0/DMIC_DATA2H_I2S0_DOUT0H_I2S0_DIN1/PI_EINT3	P13/RGMII_RXD0/RMII_RXD0/DMIC_DATA2H_I2S0_DOUT0H_I2S0_DIN1/PI_EINT3
P14/RGMII_RXCTL/RMII_CRS_DVIL/UART2_RX/TX0_S0_ERR/TW10_SDA/PI_EINT4	P14/RGMII_RXCTL/RMII_CRS_DVIL/UART2_RX/TX0_S0_ERR/TW10_SDA/PI_EINT4
P15/RGMII_RXC/RMII_RXC/NULL/UART2_RX/TX0_S0_SYNC/TW11_SCK/PI_EINT5	P15/RGMII_RXC/RMII_RXC/NULL/UART2_RX/TX0_S0_SYNC/TW11_SCK/PI_EINT5
P16/RGMII_RXC/RMII_RXC/NULL/UART2_RX/TX0_S0_D0/TW10_SDA/PI_EINT6	P16/RGMII_RXC/RMII_RXC/NULL/UART2_RX/TX0_S0_D0/TW10_SDA/PI_EINT6
P17/RGMII_RXD3/RMII_RXD3/NULL/UART2_RTS/TX0_S0_D0/TW10_SCK/PI_EINT7	P17/RGMII_RXD3/RMII_RXD3/NULL/UART2_RTS/TX0_S0_D0/TW10_SCK/PI_EINT7
P18/RGMII_RXD2/RMII_RXD2/NULL/UART2_CTS/TX0_S0_D0/DVL/TW11_SDA/PI_EINT8	P18/RGMII_RXD2/RMII_RXD2/NULL/UART2_CTS/TX0_S0_D0/DVL/TW11_SDA/PI_EINT8
P19/RGMII_RXD1/RMII_RXD1/NULL/UART2_TX/TX0_S0_D0/TW12_SCK/PI_EINT9	P19/RGMII_RXD1/RMII_RXD1/NULL/UART2_TX/TX0_S0_D0/TW12_SCK/PI_EINT9
P10/RGMII_RXD0/RMII_RXD0/NULL/UART2_RX/TX0_S0_D1/TW12_SDA/PI_EINT10	P10/RGMII_RXD0/RMII_RXD0/NULL/UART2_RX/TX0_S0_D1/TW12_SDA/PI_EINT10
P11/RGMII_RXC/RMII_RXC/NULL/UART3_RX/TX0_S0_D2/PWM1/PI_EINT11	P11/RGMII_RXC/RMII_RXC/NULL/UART3_RX/TX0_S0_D2/PWM1/PI_EINT11
P12/RGMII_RXC/RMII_RXC/NULL/UART3_CTS/TX0_S0_D3/PWM2/PI_EINT12	P12/RGMII_RXC/RMII_RXC/NULL/UART3_CTS/TX0_S0_D3/PWM2/PI_EINT12
P13/RGMII_CLKN/RMII_CLKN/NULL/UART4_RX/TX0_S0_D4/PWM3/PI_EINT13	P13/RGMII_CLKN/RMII_CLKN/NULL/UART4_RX/TX0_S0_D4/PWM3/PI_EINT13
P14/MDCI/UART4_RX/TX0_S0_D5/PWM4/PI_EINT14	P14/MDCI/UART4_RX/TX0_S0_D5/PWM4/PI_EINT14
P15/MDCI/UART4_CTS/TX0_S0_D6/CLKL_FANOUT0/PI_EINT15	P15/MDCI/UART4_CTS/TX0_S0_D6/CLKL_FANOUT0/PI_EINT15
P16/MDCI/UART4_DTS/TX0_S0_D7/CLKL_FANOUT1/PI_EINT16	P16/MDCI/UART4_DTS/TX0_S0_D7/CLKL_FANOUT1/PI_EINT16
P17/RGMII_RXD3/RMII_RXD3/NULL/UART4_RTS/TX0_S0_D8/CLKL_FANOUT2/PI_EINT17	P17/RGMII_RXD3/RMII_RXD3/NULL/UART4_RTS/TX0_S0_D8/CLKL_FANOUT2/PI_EINT17
P18/RGMII_RXD2/RMII_RXD2/NULL/UART4_SCK/TX0_S0_D9/CLKL_FANOUT3/PI_EINT18	P18/RGMII_RXD2/RMII_RXD2/NULL/UART4_SCK/TX0_S0_D9/CLKL_FANOUT3/PI_EINT18
P19/MDCI/UART4_SCK/TX0_S0_D10/CLKL_FANOUT4/PI_EINT19	P19/MDCI/UART4_SCK/TX0_S0_D10/CLKL_FANOUT4/PI_EINT19

H616-BGA-284

