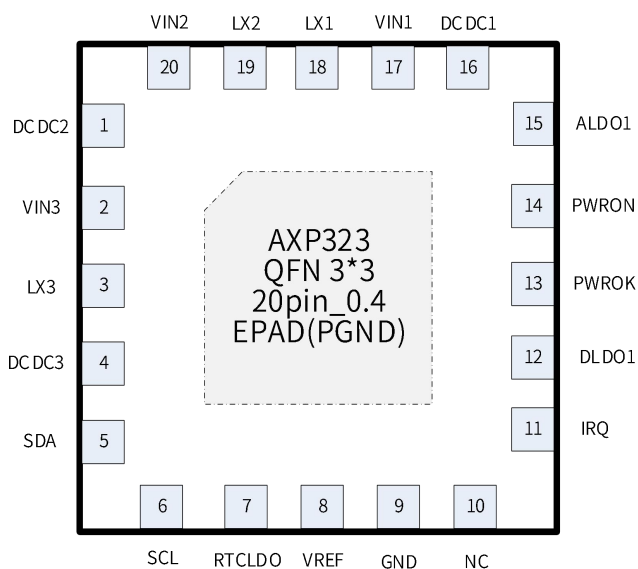


4 Pin Configuration and Functions

Figure 4-1 Pin Map

Table 4-1 Pin Description

Pin		I/O(1)	Description
No	Name		
1	DCDC2	I	DCDC2 feedback pin
2	VIN3	PI	DCDC3 input source
3	LX3	PO	Inductor pin for DCDC3
4	DCDC3	I	DCDC3 feedback pin
5	SDA	IO	Data pin for serial interface
6	SCL	IO	Clock pin for serial interface
7	RTCLDO	PO	Can be customized as a always on LDO for powering the RTC module
8	VREF	AIO	Internal reference voltage, external 1uF capacitor
9	GND	GND	GND for internal analog circuit
10	NC	/	Keep floating
11	IRQ	IO	Interrupt Indication / Power On / Wake Up
12	DLDO1	PO	Output pin of DLDO1
13	PWROK	IO	Power good indication output
14	PWRON	I	Power On-Off key input Can be customized as EN pin
15	ALDO1	PO	Output pin of ALDO1
16	DCDC1	I	DCDC1 feedback pin
17	VIN1	PI	DCDC1 input source
18	LX1	PO	Inductor pin for DCDC1
19	LX2	PO	Inductor pin for DCDC2
20	VIN2	PI	DCDC2 input source

(1) **O** for output, **I** for input, **IO** for input/output, **D** for digital, **A** for analog, **P** for power, and **G** for ground.