SYMBOL	DESCRIPTION	CONDITIONS	MIN	TYP	MAX	UNIT
V_{OFF}	PMIC Under Voltage Power off			2.6		V
Off Mode Current						
I_{OFF}	OFF Mode Current	V_{IN} =5V		10		μA
TWSI						
VCC	Input Supply Voltage		1.8	3.3		V
ADDRESS	TWSI Slave Address (7 bits)			0x36/ 0x37		
f_{SCK}	Clock Operating Frequency			0.4		MHz
V_{IL}	SCK/SDA Logic Low Voltage				0.3VCC	V
V_{IH}	SCK/SDA Logic Low Voltage		0.7VCC			MHz
DCDC						
f_{OSC}	Oscillator Frequency			1.5		MHz
Efficiency		5V-0.9V-1A		82%		
DCDC1						
V_{IN1}	VIN1 Input Voltage		V_{OFF}		5.5	V
I_{DC1OUT}	Available Output Current	V_{IN1} =5V		3000		mA
V_{DC1OUT}	Output Voltage Range		0.5		3.4	V
V_{DC1_STEP}	Output Voltage Step	V_{DC1OUT}=0.5~1.2V		10		mV/ step
		V_{DC1OUT}=1.22~1.54V		20		
		V_{DC1OUT}=1.6~3.4V		100		
V_{DC1_ACC}	Output Voltage Accuracy	V_{DC1OUT}<=1V, CCM mode		±30		mV
		V_{DC1OUT}>1V, CCM mode		±3%		
V_{DC1_OVP}	Over Voltage Protection			120%* V_{DC1OUT}		V
V_{DC1_UVP}	Under Voltage Protection			85%* V_{DC1OUT}		V
DCDC2						
V_{IN2}	VIN2 Input Voltage		V_{OFF}		5.5	V
I_{DC2OUT}	Available Output Current	V_{IN2} =5V		3000		mA
V_{DC2OUT}	Output Voltage Range		0.5		1.54	V
V_{DC2_STEP}	Output Voltage Step	V_{DC2OUT}=0.5~1.2V		10		mV/ step
		V_{DC2OUT}=1.22~1.54V		20		
V_{DC2_ACC}	Output Voltage Accuracy CCM mode	V_{DC2OUT}<=1V, CCM mode		±30		mV
		V_{DC2OUT}>1V, CCM mode		±3%		
V_{DC2_OVP}	Over Voltage Protection			120%* V_{DC2OUT}		V
V_{DC2_UVP}	Under Voltage Protection			85%* V_{DC2OUT}		V
DCDC3						