

6.3 TWSI Communication

When AXP323 powers on, SCK/SDA pin of TWSI will be pulled up to IO Power and then Host can adjust and monitor AXP323 with rich feedback information. The default slave address is 0x6C/0x6D.

Note: “Host” here refers to system processor.

Figure 6-2 Waveform of TWSI

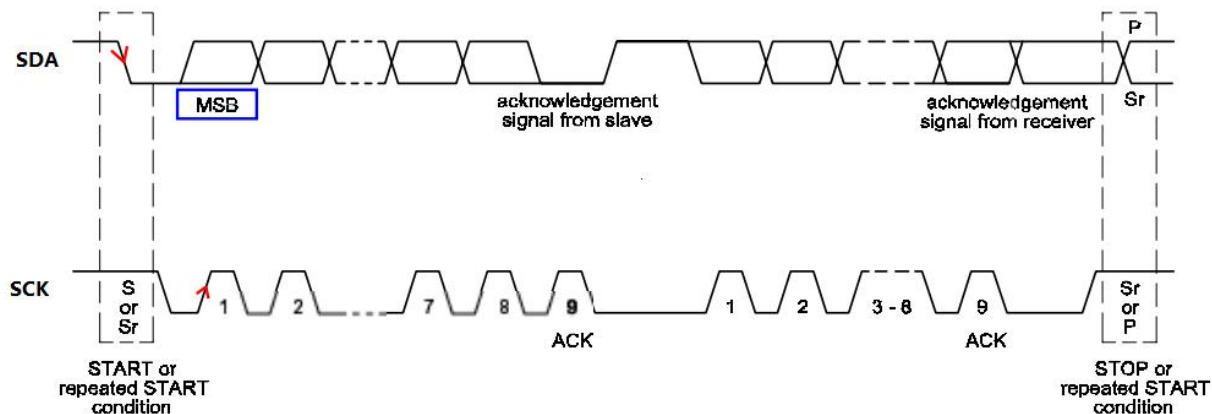


Table 6-1 Read and write address of TWSI

BYTE	BIT							
	MSB	6	5	4	3	2	1	0
WRITE	0	1	1	0	1	1	0	0
READ	0	1	1	0	1	1	0	1

Figure 6-3 Timing of TWSI

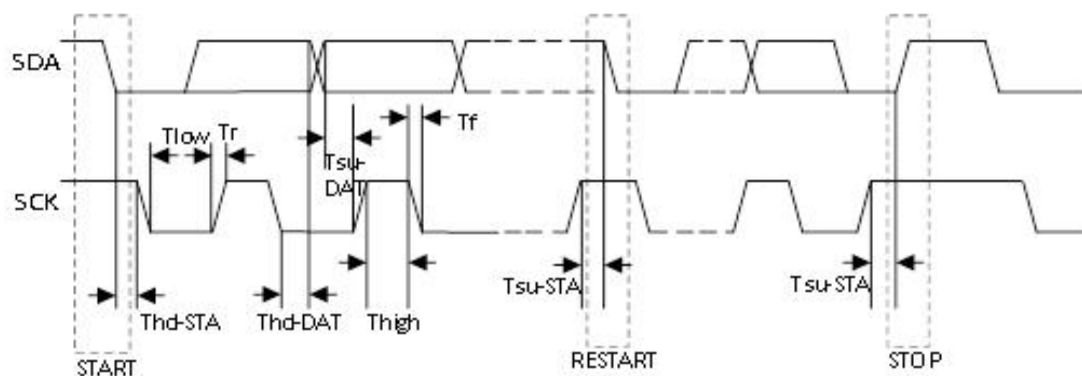


Table 6-2 Timing constants

Parameter	Symbol	Stander mode			Fast mode			Unit
		MIN	TYP	MAX	MIN	TYP	MAX	
SCK clock frequency	Fsck	0	-	100	0	-	400	kHz
Setup time in START	Tsu-STA	4.7	-	-	0.6	-	-	us
Hold time in START	Thd-STA	4.0	-	-	0.6	-	-	us
Setup time in Data	Tsu-DAT	250	-	-	100	-	-	ns
Hold time in Data	Thd-DAT	0	-	-	0	-	0.9	us
Setup time in STOP	Tsu-STO	4.0	-	-	0.6	-	-	us
SCK low level time	Tlow	4.7	-	-	1.3	-	-	us
SCK high level time	Thigh	4.0	-	-	0.6	-	-	us