

Parameter	Symbol	Stander mode			Fast mode			11
		MIN	ТҮР	MAX	MIN	ТҮР	MAX	Unit
SDA/SCK falling time	Tf	-	-	300	20	-	300	ns
SDA/SCK rising time	Tr	-	-	1000	20	-	300	ns

6.4 Power On/Off & Reset

AXP323 has power off and power on status. When at off state, all voltage outputs are turned off except RTCLDO (If customized as always on). The total power consumption is typically 6uA(if RTCLDO is always on, consumption is about 10uA).

6.4.1 Power on-off Key (POK)

EN/PWRON pin can be configured as PWRON pin or EN pin by customization. The default is PWRON pin. The Power on-off Key (POK) can be connected between PWRON pin and GND of AXP323. AXP323 can automatically identify the four status(Long-press, Short-press, Negative edge, Positive edge) and then correspond respectively.

6.4.2 Power on

1. When EN/PWRON pin is customized as PWRON pin, power on sources include:

(1).POK. AXP323 can be powered on by pressing and holding POK for a period of time that longer than "ONLEVEL".

(2).VIN low to high. The function can be configured by customization.

(3).IRQ Low level. IRQ pin is low level for more than 16ms. The power-on function can be customized.

2.When EN/PWRON pin is customized as EN pin , AXP323 can be powered on by EN pin from low to high(>1.2V).

After power on, DCDC and LDO will be soft booted in preset timing sequence.

Figure 6-4 Power On Sequence

V _{PWR-ON} VIN	_
Power On Input Voltage	
N	
TCLDO	
ternal POWER EN	
CDC/LDO OUTPUT	
Tstartup-interval	
WROK TPWROK-dly Option: 8/32ms	

6.4.3 Power Off

1. When EN/PWRON pin is customized as PWRON pin, power off sources include:

(1).POK. AXP323 can be powered off by pressing and holding POK for a period of time that longer than "OFFLEVEL". The function can be configured by REG1BH[1] and REG1BH[0] decides whether the PMIC auto turns on or not when it shuts down after OFFLEVEL POK.

(2).Write "1" to REG1AH[7].

(3).VIN high to low. When VIN<VOFF, AXP323 will be powered off. The default of V_{OFF} is 2.6V.

(4).VIN>5.8V.