(5).The output voltage of DCDC is 15% lower than the setting value. The function can be configured by REG1DH[3:1].

(6).The output voltage of DCDC is 20% larger than the setting value. The function can be configured by REG1DH[0].

(7).DLDO1 over-current(>1A). The function can be configured by REG1DH[4].

(8).Die temperature is over the warning level2(125℃). The function can be configured by REG1AH[1].

2.When EN/PWRON pin is customized as EN pin ,

(1).AXP323 can be powered off by EN pin from high to low.(<1.0V)

(2).The same as the power off source (3)~(8) used as PWRON pin.

Figure 6-5 Power Off Sequence
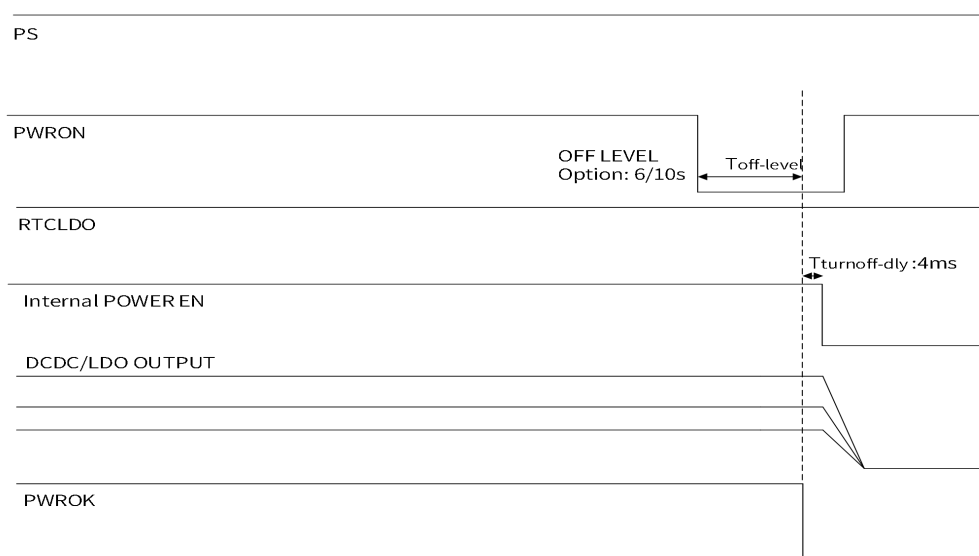
6.4.4   Sleep and wakeup

When the running system needs to enter Sleep mode, Maybe one or several power outputs should be disabled or changed to other voltage. The function can be configured by REG1CH[0]. Wakeup can be initiated by the following sources:

1.Software wakeup (REG1CH[1] is set to 1).

2.IRQ pin wakeup (REG1CH[4]=1 and IRQ pin is low level for more than 16ms by triggering an IRQ interrupt (POKPIRQ (REG20H[7]=1) or POKPNRQ (REG20H[6]=1) or POKSIRQ (REG20H[5]=1) or POKLIRQ (REG20H[4]=1) or DCDC3 UVP interrupt (REG20H[3]=1) or DCDC2 UVP interrupt (REG20H[2]=1) or OVP interrupt(REG 20H[0]=1)); or externally pull the IRQ pin low for more than 16ms.

These sources will make the all PMIC power outputs resume to the default voltage or the setting voltage, which is configured by REG1CH[2], and all shutdown powers will resume by the startup sequence.

See the control process under sleep and wakeup modes as below.

Figure 6-6 Sleep and wakeup