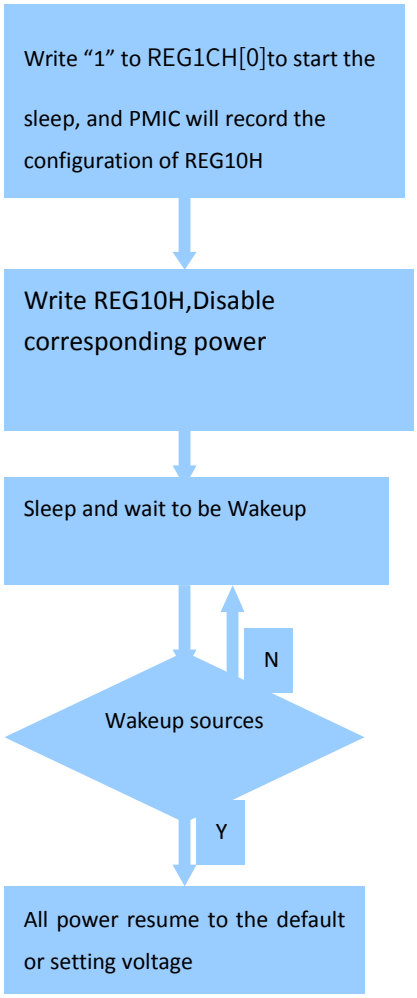

6.4.5 Reset

The PMIC has system reset and power on reset(POR).

- System reset

System reset means the PMIC is power off first and then power on. The power off and power on sequence of each output is the same with the normal power on sequence, and the corresponding registers are reset. During system reset, RTCLDO is always on (if RTCLDO is customized to be always on). There are two ways of system reset.

(1).PWROK drive low.

The PWROK pin can be used as the reset signal of application system. During AXP323 startup, PWROK outputs low level, which will be pulled up to startup the system after output voltage reaches the regulated value.

When application system works normally, If the PWROK pin is driven low by external key or other reasons, the AXP323 will be restarted. The function can be configured by REG1AH[4].

(2).Write “1” to REG1AH[6] to restart the PMIC.

- Power on reset(POR)

Power on reset means all the internal logic will be reset. When at power on reset state, all voltage outputs DCDC/LDO including RTCLDO are turned off and then turned on.

6.5 Multi-Power Outputs

The following table has listed the multi-power outputs and their functions of AXP323.

Table 6-3 Power output information

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