

Address	Description	R/W	Default
14	DCDC2 Voltage control	R/W	XXH
15	DCDC3 Voltage control	R/W	XXH
16	ALDO1 Voltage control	R/W	XXH
17	DLDO1 Voltage control	R/W	XXH
1A	Power off, Restart Control	R/W	20H
1B	Power off sequence, POK control	R/W	06H
1C	Power wakeup control	R/W	00H
1D	Output monitor control	R/W	1FH
1E	POK settings	R/W	80H
20	IRQ Enable	R/W	31H
21	IRQ Status	R/W	00H
22	DCDC mode control2	R/W	00H

6.7.2 Register Description

6.7.2.1 REG 00H: Power ON source indication

Reset: system reset

Bit	Description	R/W
7-5	Reserved	R
4	Startup by EN from L go H when VIN is High, or VIN from L go high when EN is High	R
3	Startup by IRQ pin	R
2	Startup by PWRON Press	R
1	Reserved	R
0	Startup by VIN from L go H	R

6.7.2.2 REG 10H: on-off control

Default: XXH

Reset: system reset

Bit	Description	R/W	Default
7-5	Reserved	RW	0
4	DLDO1 on-off control	0: off; 1: on	Customization
3	ALDO1 on-off control		
2	DCDC3 on-off control		
1	DCDC2 on-off control		
0	DCDC1 on-off control		

6.7.2.3 REG 12H: DCDC mode control1

Default: 00H

Reset: system reset

Bit	Description	R/W	Default
7	DCDC2 FCCM mode: 0: NOT FCCM	RW	0