

	1: FCCM		
6	DCDC3 FCCM mode: 0: NOT FCCM 1: FCCM	RW	0
5	DCDC1 FCCM mode: 0: NOT FCCM 1: FCCM	RW	0
4	DCDC2 DVM voltage ramp control 0: 1step/20us 1: 1step/40u	RW	0
3-0	Reserved	RW	0

6.7.2.4 REG 13H: DCDC1 voltage control

Default: XXH

Reset: system reset

Bit	Description	R/W	Default
7	Reserved	RW	0
6-0	DCDC1 voltage setting bit4-0: 0.5~1.2V, 10mV/step, 71steps 1.22~1.54V, 20mV/step, 17steps 1.6~3.4V, 100mV/step, 19 step	RW	Customization

6.7.2.5 REG 14H: DCDC2 voltage control

Default: XXH

Reset: system reset

Bit	Description	R/W	Default
7	DCDC2 DVM on-off control 0: disable 1: enable	RW	0
6-0	DCDC2 voltage setting bit6-0: 0.5~1.2V, 10mV/step, 71steps 1.22~1.54V, 20mV/step, 17steps	RW	Customization

6.7.2.6 REG 15H: DCDC3 voltage control

Default: XXH

Reset: system reset

Bit	Description	R/W	Default
7	Reserved	RW	0
6-0	DCDC3 voltage setting bit6-0: 0.5~1.2V, 10mV/step, 71steps 1.22~1.84V, 20mV/step, 32steps	RW	Customization

6.7.2.7 REG 16H: ALDO1 voltage control

Default: XXH