Reset: system reset

Bit	Description	R/W	Default
7-5	Reserved	RW	0
4-0	ALDO1 voltage setting bit4-0: 0.5~3.5V, 100mV/step, 31steps	RW	Customization

6.7.2.8 REG 17H: DLDO1 voltage control

Default: XXH

Reset: system reset

Bit	Description	R/W	Default
7-5	Reserved	RW	0
4-0	DLDO1 voltage setting bit4-0: 0.5~3.5V, 100mV/step, 31steps	RW	Customization

6.7.2.9 REG 1AH: Power off sequence,POK control

Default: 20H

Reset: bit [7:6] is System reset,other bits is power on reset

Bit	Description	R/W	Default
7	Soft power off control. Write 1 to this bit will power off the PMIC, and this bit will clear itself	RW	0
6	Soft restart control. Write 1 to this bit will restart the PMIC, and this bit will clear itself	RW	0
5	Enable for PMIC to monitor the status of PWROK pin to judge whether PMIC starts up normally 0: disable 1: enable	RW	1
4	Enable to restart the PMIC by PWROK drive low 0: disable 1: enable	RW	0
3-2	Reserved	RW	0
1	The PMIC shut down or not when die temperature is over the warning level 2 0: not shutdown 1: shutdown	RW	0
0	Over temperature protection threshold configuration 0: 125°C 1: 145°C	RW	0

6.7.2.10 REG 1BH: Power off sequence、pwron key off control

Default: 06H

Reset: Power on reset

Bit	Description	R/W	Default
7-4	Reserved	RW	0000
3	Output power down sequence control 0: at the same time 1: the reverse of the startup	RW	0
2	Enable for 4ms delay when PMIC power off normally	RW	1