

	0: disable 1: enable		
1	Enable bit for the function which will shut down the PMIC when POK is larger than OFFLEVEL 0: disable 1: enable	RW	1
0	The PMIC auto turn on or not when it shut down after OFFLEVEL POK 0: disable 1: enable	RW	0

6.7.2.11 REG 1CH: Power wake up Control

Default: 00H

Reset: bit[3]&[1:0] is System reset, other bits are Power on reset

Bit	Description	R/W	Default
7-5	Reserved	RW	0
4	IRQ Pin low to wakeup 0 : disable 1 : enable	RW	0
3	PWROK drive low or not when Power wake up 0: not drive low 1: drive low in wake up period	RW	0
2	Voltage recovery control when the PMIC wakeup 0: recovery to the default 1: Do nothing to the voltage(depend on the voltage before wakeup)	RW	0
1	Software wakeup write 1 to this bit, the output power will be waked up, and this bit will clear itself	RW	0
0	Sleep function enable	RW	0

6.7.2.12 REG 1DH: Output monitor control

Default: 1FH

Reset: Power on reset

Bit	Description	R/W	Default
7-5	Reserved	RW	000
4	DLDO1 over current turn off PMIC function 0: disable 1: enable	RW	1
3	DCDC3 under voltage turn off PMIC function 0: disable 1: enable	RW	1
2	DCDC2 under voltage turn off PMIC function 0: disable 1: enable	RW	1
1	DCDC1 under voltage turn off PMIC function 0: disable 1: enable	RW	1
0	If voltage of DCDC is over 20%(30%) of the setting value, the PMIC shutdown or not. (OVP) 0: not shutdown 1: shutdown	RW	1