

6.7.2.13 REG 1EH: POK setting

Default: 80H

Reset: Power on reset

Bit	Description	R/W	Default
7	Reserved	RW	1
6-2	Reserved	RW	0
1	OFFLEVEL setting 0: 6s 1: 10s	RW	0
0	IRQLEVEL setting 0: 1.5s 1: 2.5s	RW	0

6.7.2.14 REG 20H: IRQ Enable

Default: 31H

Reset: System reset

Bit	Description	R/W	Default
7	PWRON positive edge IRQ enable	RW	0
6	PWRON negative edge IRQ enable	RW	0
5	PWRON short press IRQ enable	RW	1
4	PWRON long press IRQ enable	RW	1
3	DCDC3 under voltage IRQ enable	RW	0
2	DCDC2 under voltage IRQ enable	RW	0
1	Reserved	RW	0
0	Die over temperature protection IRQ enable	RW	1

6.7.2.15 REG 21H: IRQ Status

Default: 00H

Reset: System reset

Bit	Description	R/W	Default
7	PWRON positive edge IRQ,write 1 to clear it	RW	0
6	PWRON negative edge IRQ,write 1 to clear it	RW	0
5	PWRON short press IRQ,write 1 to clear it	RW	0
4	PWRON long press IRQ,write 1 to clear it	RW	0
3	DCDC3 under voltage IRQ,write 1 to clear it	RW	0
2	DCDC2 under voltage IRQ,write 1 to clear it	RW	0
1	Reserved	RW	0
0	Die over temperature protection IRQ,write 1 to clear it	RW	0

6.7.2.16 REG 22H: DCDC mode control2

Default: 00H

Reset: Power on reset