

## 7 Application Information

### 7.1 DCDC/LDO Design

1. Output capacitance of LDO is not smaller than 1uF.
2. For unused LDO, the output pin just stays floating and dose not need an external capacitor.
3. Each DCDC uses a 1uH inductor. Its saturation current of inductor should be 30% higher than the load current, and the internal resistance is less than 30mohm.
4. Output capacitance of each DCDC is not smaller than 10uF.

### 7.2 IO Design

1. TWSI: Pull up SDA and SCK to a source, such as RTCLDO.
2. Pull up IRQ with a 4.7k $\Omega$  resistor to a source, such as RTCLDO.
3. If the system needs a reset key, just connect a key between PWROK pin and GND.
4. Use RC(510 $\Omega$ +100nF) to connect the PWRON key between PWRON pin and GND